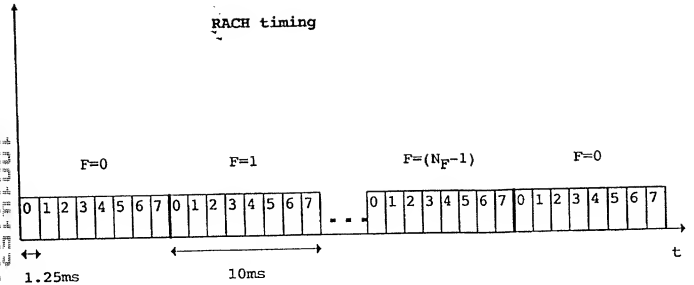
RACH timing
F=0
F=1
F=(N_F-1)
F=0
0 1 2 3 4 5 6 7
0 1 2 3 4 5 6 7
0 1 2 3 4 5 6 7
0 1 2 3 4 5 6 7
t
1.25ms
10ms

Fig. 1

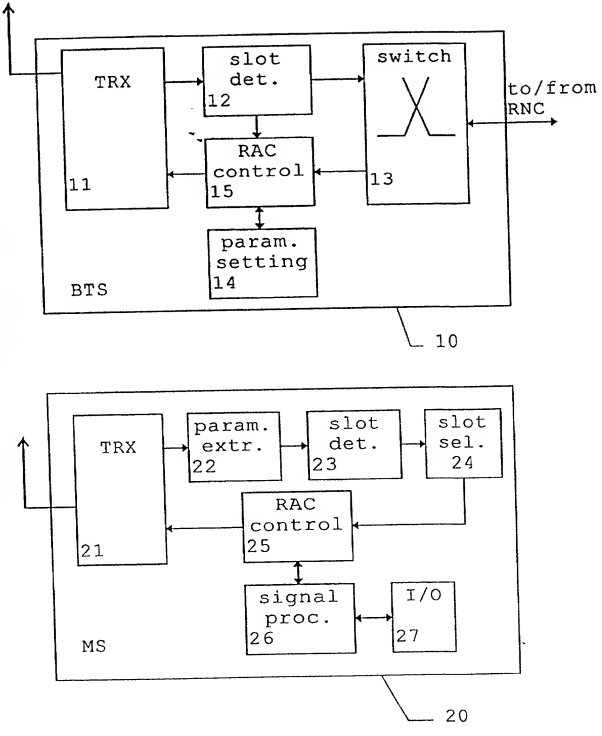
TRX
11
slot
det.
12
switch
13
to/from
RNC
RAC
control
15
param.
setting
14
BTS
10
TRX
21
param.
extr.
22
slot
det.
23
slot
sel.
24
RAC
control
25
signal
proc.
26
I/O
27
MS
20

Fig. 2

$T_{AICH}=0$

Fmod3	A=0 (000)	A=1 (001)	A=2 (010)	A=4 (100)
0	-	0, 3, 6	1, 4, 7	2, 5
1	-	1, 4, 7	2, 5	0, 3, 6
2	-	2, 5	0, 3, 6	1, 4, 7

Fig. 3

$T_{AICH}=1$

A=0 (0000)	A=1 (0001)	A=2 (0010)	A=4 (0100)	A=8 (1000)
-	0, 4	1, 5	2, 6	3, 7

Fig. 4

$T_{AICH}=0$

A=0 (000)	A=1 (001)	A=2 (010)	A=4 (100)
-	0, 3, 6, 9, 12	1, 4, 7, 10, 13	2, 5, 8, 11, 14

Fig. 5

$T_{AICH}=1$

Γmod4	A=0 (0000)	A=1 (0001)	A=2 (0010)	A=4 (0100)	A=8 (1000)
0	-	0, 4, 8, 12	1, 5, 9, 13	2, 6, 10, 14	3, 7, 11
1	-	1, 5, 9, 13	2, 6, 10, 14	3, 7, 11	0, 4, 8, 12
2	-	2, 6, 10, 14	3, 7, 11	0, 4, 8, 12	1, 5, 9, 13
3	-	3, 7, 11	0, 4, 8, 12	1, 5, 9, 13	2, 6, 10, 14

Fig. 6